Q1 Power subsystem [30]

Q1.1 Specification [10]

Q1.2 Draft Bill Of Materials (BOM) [10]

Q1.3 Define this submodule’s interface(s) [10]

Q2 Microcontroller interfacing [30]

Q2.1 Specification [10]

Q2.2 Draft Bill Of Materials (BOM) [10]

Q2.3 Define this submodule’s interface(s) [10]

Q3 Sensing [40]

Q3.1 Specification [20]

Q3.1.1 Digital Sensor Specification [10]

Q3.1.2 Analog Sensor Specification [10]

Q3.2 Draft Bill Of Materials (BOM) [10]

Q3.3 Define this submodule’s interface(s) [10]

Q3.3.1 Digital Sensor Interface [5]

Q3.3.2 Analog Sensor Interface [5